

**NOTIFICATIONS FOR ADMISSIONS TO VARIOUS PH.D PROGRAMMES FOR
THE ACADEMIC YEAR-2022-23 UNDER
VISVESVARAYA PHD SCHEME FOR ELECTRONICS AND IT PHASE - II**

1. The following candidates can register through online SAMARTH registration portal for Ph.D. Admission and eligible candidates shall appear for written test and personal interaction:
 - i. Candidates with PG in relevant / allied subject as per the eligibility criteria mentioned at Sl. No.8 of the Notification.
 - ii. Candidates who have qualified national level examinations such as JRF [UGC/ CSIR / DBT / ICMR / ICSSR), NET (UGC/CSIR), GATE or equivalent etc] shall be given preference.
 - iii. Candidates who have secured DST-INSPIRE Fellowship.
 - iv. Candidates with Central Government and State Government sponsored fellowships.
 - v. National/State level Teacher fellowship holders.
 - vi. Candidates admitted under various research projects.
2. The candidate must register for Ph.D admission through online SAMARTH registration portal. Submission of online registration form is mandatory for the Ph.D admissions. The candidates are advised to refer to the user manual for online registration on the SAMARTH portal.
3. All candidates are required to pay the non-refundable registration fee as per the following categories.

Category	Non-refundable registration fee (Rs.)*
General, OBC (Non-creamy layer), EWS	500-00
Female	250-00
SC/ST	100-00

(Registration fee is excluding transaction charges)

4. The tentative schedule for registration, declaration of eligible candidates, the dates of personal interaction of admission are as follows :

Item No.	Tentative time
Date of registration	21/12/22 to 27/12/22 upto 2:00PM
Date of declaration of eligible	27/12/2022 4:00 PM
Date of addressing grievance	27/12/2022 6:00 PM
Date of Written test followed by Personal Interaction	28/12/2022 10:00AM
Date of declaration of selected candidates	28/12/2022 6:00 PM
Date of admissions and fee payment	29/12/2022
Date of commencement of classes	02/01/2023

5. A list of candidates whose applications are received with the prescribed fee along with the details will be scrutinized and the eligible candidates will be informed through e-mail.
6. The eligible candidates shall appear for the written test and personal interaction as per the schedule. Students having grievances, if any, regarding their eligibility may contact the concerned department.
7. The candidates applying under the reserved categories shall submit the Category Certificates (SC/ST/OBC Non-Creamy Layer Certificate/ EWS) issued by the competent authority in the prescribed format at the time of registration and produce the same in original for verification at the time of admission. The candidates are advised to visit the website for concerned certificate format available in the University website <https://www.cuk.ac.in>
8. The eligibility criteria for various Ph.D programmes are as follows-

PROGRAMME	ELIGIBILITY CRITERIA / MINIMUM QUALIFICATION FOR ADMISSION
Ph.D. – Computer Science	M.Phil. with course work or Master's degree in Computer Science/ Master of Computer Applications or equivalent degree from any University/Institute recognized by the UGC / MHRD or foreign university (as per the AIU foreign equivalence list) with at least 55% marks (50% for SC, ST, OBC {NON CREAMY LAYER}, PWD and Kashmiri Migrant and non-migrants Students), preceded by a Bachelor's Degree or an
Ph.D. – Electronics and Communication	M.E/M.Tech degree in Electronics and Communication/ Electronics/ Communication/ Microwave Engineering from any University/ Institute recognized by the UGC / MHRD / AICTE or foreign university (as per the AIU foreign equivalence list) with at least 55% marks (50% for SC, ST, OBC {NON CREAMY LAYER}, PWD and Kashmiri Migrant and non-migrants Students), preceded by a Bachelor's Degree or an equivalent Degree of four years duration from a recognized University/Institute.
Ph.D - Electrical Engineering	M.E/M.Tech degree in Electrical Engineering / Energy / Power Systems or Equivalent in Electrical Engineering discipline from any University/ Institute recognized by the UGC / MHRD / AICTE or foreign university (as per the AIU foreign equivalence list) with at least 55% marks (50% for SC, ST, OBC {NON CREAMY LAYER}, PWD and Kashmiri Migrant and non-migrants Students), preceded by a Bachelor's Degree in Electrical Engineering or an equivalent Degree of four years duration from a recognized University/Institute.

9. The selection process for PhD admission is as follows: 70% weightage shall be given to written test and score secured in JRF, NET, GATE, allied exams. 30% weightage shall be given for personal interaction. The tentative distribution of marks are as follows.

Weightage	Criteria	Range	Total marks
70%	Written test	Written test	Max. 50 marks
	JRF (UGC/CSIR/DBT/GATE..etc)	JRF (UGC/CSIR/DBT..etc): 20 marks DST-INSPIRE/other. Fellowships: 15 marks NET (UGC/CSIR), GATE qualified etc : 10 marks	Max. 20 marks
30%	Personal interaction	Presentation : 10 marks	Max. 10 marks
		Interaction : 20 marks	Max. 20 marks

10. Candidates securing a minimum of 50% marks (45% for SC, ST, OBC {NON CREAMY LAYER}, EWS) in the combined score of the 70% and 30% weightage marks calculations will be eligible to be considered for admission as per the merit list.
11. The University reserves the right not to offer the programme in any particular discipline due to administrative reasons or if the response to the programme is not adequate. The decision of the University will be final in this regard.
12. Reservation/relaxation will be given as per Government of India/University Rules as adopted by the University time to time.
13. Ensuring the eligibility for applying to a particular programme will be the sole responsibility of the candidates. Please read the eligibility criteria and other requirements before submitting the application form. Submission of online registration form and attending the written test and personal interaction are mandatory for the admission to the Ph.D programme.
14. These instructions are subject to alterations & modifications and the same will be notified on the University website. These instructions are for information only. Candidates are advised to visit the University website regularly for further information and instructions.


Registrar
Registrar
Central University of Karnataka
Kalaburagi-585 367

Copy to

1. PS to VC
2. Academic Section and Examination Section
3. Central Admission Committee
4. Rajbhasha section for translation
5. System Analyst is requested to upload on the University website
6. Concerned Departments
7. Concerned file.

PhD Scheme

Visvesvaraya PhD Scheme for Electronics & IT

Ministry of Electronics and Information Technology, Government of India initiated “Visvesvaraya PhD Scheme for Electronics and IT” with an objective to enhance the number of PhDs in Electronics System Design & Manufacturing (ESDM) and IT/IT Enabled Services (IT/ITES) sectors in the country.

Cabinet Committee on Economic Affairs (CCEA) approved the scheme in 2014 for a period of nine years with a total estimated cost of ₹ 466 crores (Rupees Four Hundred Sixty Six Crores Only)

Objectives:

- To enhance the number of PhDs in Electronics System Design & Manufacturing (ESDM) and IT/IT Enabled Services (IT/ITES) sectors in the country.
- Give thrust to R&D, create an innovative ecosystem and enhance India's competitiveness in these knowledge intensive sectors.
- To fulfil the commitments made in National Policy on Electronics (NPE 2012) and National Policy on Information Technology (NPIT 2012) which recommend to give special thrust on significantly increasing the number of PhDs in the country to enable India to compete globally in the coming decades, to develop an ecosystem of research, development and IP creation in these knowledge intensive sectors.
- To Support 1500 PhD Candidates including both Full-Time (500) and Part-Time (1000) in each of ESDM and IT/ITES sectors (Total: 3000 PhDs).
- The scheme is also expected to encourage working professionals and non-PhD faculty members to pursue PhD.

Salient features of the PhD Scheme are as follows:

- This Scheme provides 25% more fellowship amount than most of the other PhD Schemes.
- Scheme also provides infrastructural grant of ₹ 5,00,000/- per candidate to the academic institutions for creation/ up-gradation of laboratories.
- Part-time PhD candidates get one time incentive on completion of the PhD.
- Scheme also supports 200 Young Faculty Research Fellowships in the areas of ESDM and IT/ITES with the objective to retain and attract bright young faculty members in these sectors.
- **Full Time PhD Candidates:**
 - Fellowship : ₹ 31,500 p.m.(revised as ₹ 38,750/- pm w.e.f. 1-4-2019) (I & II yr); ₹ 35,000 (revised as ₹ 43,750/- pm w.e.f. 1-4-2019)(III to V yr);
 - Annual Contingency Grant support ₹ 30,000/- per year for consumables etc.
 - Reimbursement of Rent for those PhD candidates who are not provided accommodation or choose not to avail the accommodation provided by the institution.
 - Support for attending international conferences (1000 supports up to ₹ 50,000/- per conference). Any individual full-time PhD candidate would be eligible to avail the support for attending a maximum of 2 conferences under this scheme.